

Workshop on Modern Circuits and Systems Technologies

March 16, 2012, Aristotle University Dissemination Center (ΚΕΔΕΑ)

Jordan Europe Wide Enhanced research Links in ICT
INCO, ERA-WIDE

Jewel 

Workshop Program

Registration

9:30 – 9:45

9:45 – 10:00

Opening Speech

Dr. Spyridon Nikolaidis – Aristotle University of Thessaloniki

Dr. Abdoul Rjoub – Jordan University of Science and Technology

10:00 – 10:45

Keynote Lecture

Dr. Christos Savvas Bouganis – Imperial College London

“Computing with FPGAs”

Coffee Break

11:15 – 11:35

Prof. Charalampos Dimitriadis – Aristotle University of Thessaloniki

“Compact modeling of nano-scale FinFETs”

11:35 – 11:55

Dr. Mohammad Malkawi – Jadara University, Jordan

“Optimization on Nanoscale Transistor Design”

11:55 – 12:15

Dr. Spyridon Blionas – University of Peloponnese

“Design methodology for implementation of the physical layer of multi-standard Reconfigurable Systems-on-Chip (RSoCs) for Wireless Communications”

12:15 – 12:35

Dr. Nikolaos Bellas – University of Thessaly

“Architectural Synthesis using Massively Parallel Programming Models like OpenCL”

12:35 – 12:55

Dr. Georgios Sirakoulis – Democritus University of Thessaly

“Design of Complex Systems using Advanced Computational Tools”

Lunch Break

13:45 – 14:00

Dr. Ilias Pappas – Aristotle University of Thessaloniki

“A new Current Program Pixel Design for AMOLED Displays implemented with Organic TFTs”

14:00 – 14:15

Calliope-Louisa Sotiropoulou – Aristotle University of Thessaloniki

“FPGA MPSoC Optimizations: An ILP Approach”

14:15 – 14:30

Liberis Voudouris – Aristotle University of Thessaloniki

“High Speed FPGA Implementation of Hough Transform for Real-Time Applications”

14:30 – 14:45

Panagiotis Chaourani – Aristotle University of Thessaloniki

“Modeling the Operation of Pass Transistor for Nanoscale Technologies”

14:45 – 15:00

Dimitrios Tzagkas – Aristotle University of Thessaloniki

“Modeling the Operation of Pass Transistor Driving CRC Loads”